A screenshot of a computer

Description automatically generated with medium confidence

Continue must be triggered to display real time SW value on 7-segment display

Run is triggered to start the Basic I/O Test 2 program

Switches are first set to 10’b 00 0000 0000

Switches are set to 10’b 00 0000 0101

(5 in decimal)

Switches are set to 10’b 00 0000 0111

(4 in decimal)

Switches are set to 10’b 00 0000 0001

(1 in decimal)

Switches are set to 10’b 00 0000 0011

(16’h0006) to jump to the start address x0006 of the Basic I/O Test 2 program

HEX0 is displaying the SW value 16’h1

HEX0 is displaying the SW value 16’h5

HEX0 is displaying the SW value 16’h4

MAR = 16’hFFFF & Writing

Store value in internal register and display instead of storing to M[xFFFF]

MAR = 16’hFFFF & Reading

Load SW value instead of M[xFFFF]

Reset the entire SLC-3 processor

All registers are cleared

FSM is set to Halted state